

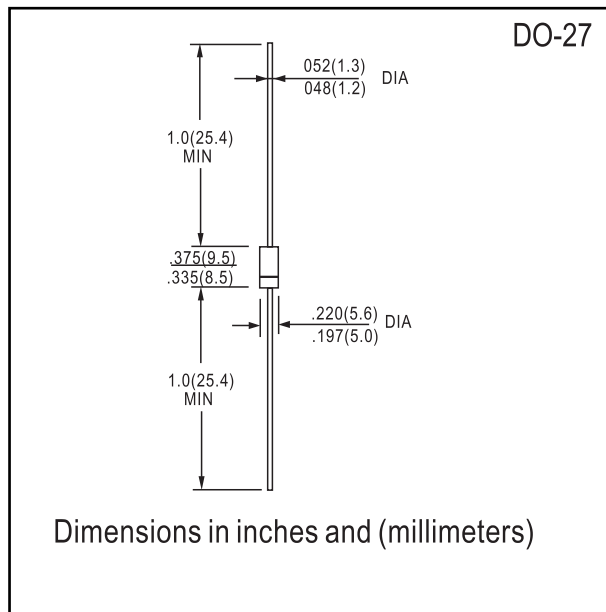


FEATURES

- High surge current capability
- Plastic package has Underwriters Laboratory Flammability Classification 94V-O
- Void-free molded plastic package
- 3.0 Ampere operation at $T_A=55$ with no thermal runaway
- Fast switching for high efficiency
- Exceeds environmental standards of MIL-S-19500/228

MECHANICAL DATA

- Case: JEDEC DO-201AD molded plastic
- Terminals: Plated Axial leads, solderable per MIL-STD-750, Method 2026
- Polarity: Color Band denotes end
- Mounting Position: Any
- Weight: .04 ounce, 1.1gram



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%

Type Number		BY396	BY397	BY398	BY399	UNITS
Maximum Repetitive Peak Reverse Voltage	VRRM	100	200	400	800	V
Maximum RMS Voltage	VRMS	70	140	280	560	V
Maximum DC Blocking Voltage	VDC	100	200	400	800	V
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Length @ $T_A = 55^\circ\text{C}$	IF(AV)	3.0				A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	IFSM	150				A
Maximum Instantaneous Forward Voltage @3.0A	VF	1.2				V
Maximum DC Reverse Current @ $T_A = 25^\circ\text{C}$ at Rated DC Blocking Voltage @ $T_A = 100^\circ\text{C}$	IR	10 150				μA μA
Maximum Reverse Recovery Time (Note 1)	TRR	250				nS
Typical Junction Capacitance (Note 2)	CJ	60				pF
Operating Temperature Range	TJ	-55 to+125				°C
Storage Temperature Range	TSTG	-55 to+150				°C

NOTES: 1. Reverse Recovery Test Conditions: $I_F=0.5\text{A}, I_R=1.0\text{A}, I_{RR}=0.25\text{A}$
2. Measured at 1 MHz and Applied Reverse Voltage of 4.0 Volts D.C.



RATINGS AND CHARACTERISTIC CURVES BY396 THRU BY399

Fig.1 - FORWARD CURRENT DERATING CURVE

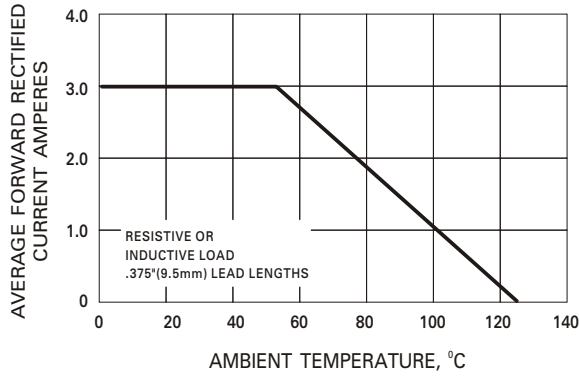


FIG.2- MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

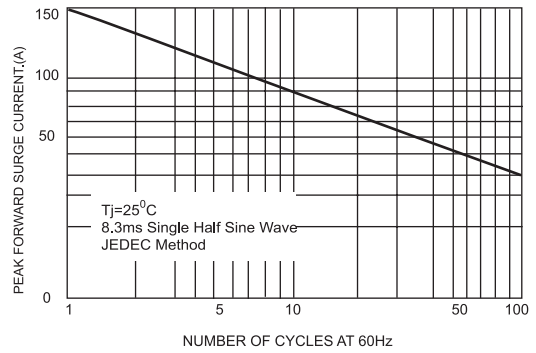


FIG.3-TYPICAL FORWARD CHARACTERISTICS

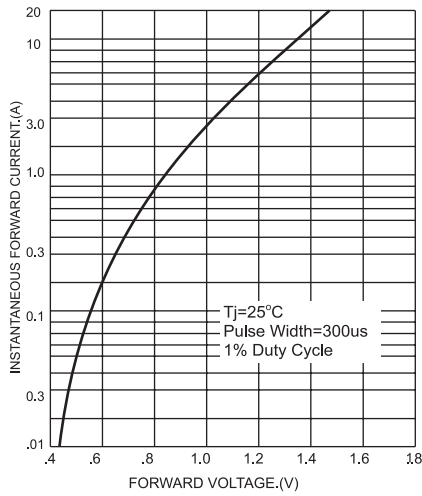


Fig.4- TYPICAL REVERSE CHARACTERISTICS

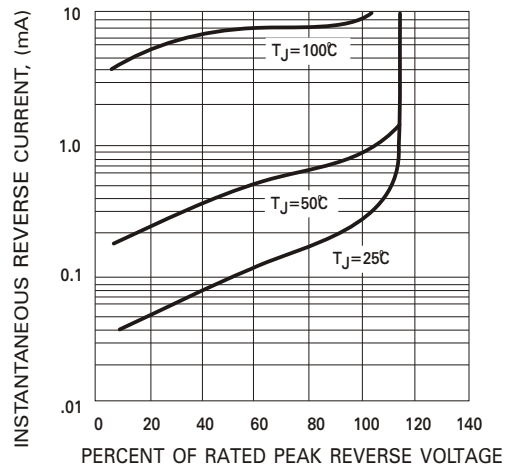


Fig.5 - TYPICAL JUNCTION CAPACITANCE

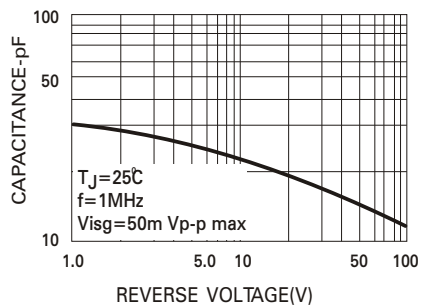


FIG.6 - REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

